

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Checkerboard buffer using two dimensional buffer pages and
using memory bank alternation

Application Number : 10/076943

Confirmation Number: 3617

First Named Applicant: Mark Champion

Attorney Docket Number: 72708/7114

Art Unit: 2671

Examiner: Dalip K. Singh

Search string: (4744046 or 5138705 or 5210614 or 6105114 or 6417848 or 6473193 or 6650332
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Certification: This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:

That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	4744046	1988-05-10	Foster			
	2	5138705	1992-08-11	Lo et al.			
	3	5210614	1993-05-11	Kannegundla et al.			
	4	6105114	2000-08-15	Okuno			
	5	6417848	2002-07-09	Battle			
	6	6473193	2002-10-29	Itoh			
	7	6650332	2003-11-18	Doyle et al.			

Signature

Examiner Name	Date